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Inventor: Sun et al.
Docket No.: ACT-362COB

Title: Method and Apparatus of Memory Clearing with Monitoring Ram Memory Cells in a...
Attorney: William P. Wilbar

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FPGA Tile 20

Fig. 1

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Attorney: William P. Wilbar

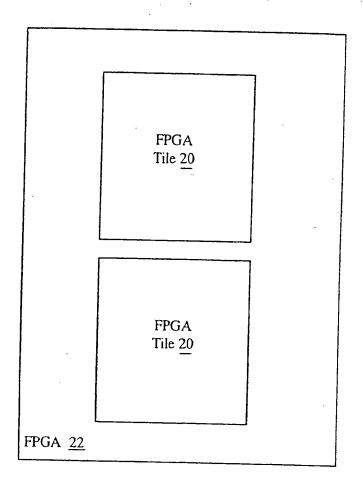


Fig. 2

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**FPGA FPGA** Tile 20 Tile 20 **FPGA** FPGA Tile 20 Tile 20 FPGA <u>24</u>

Fig.3A

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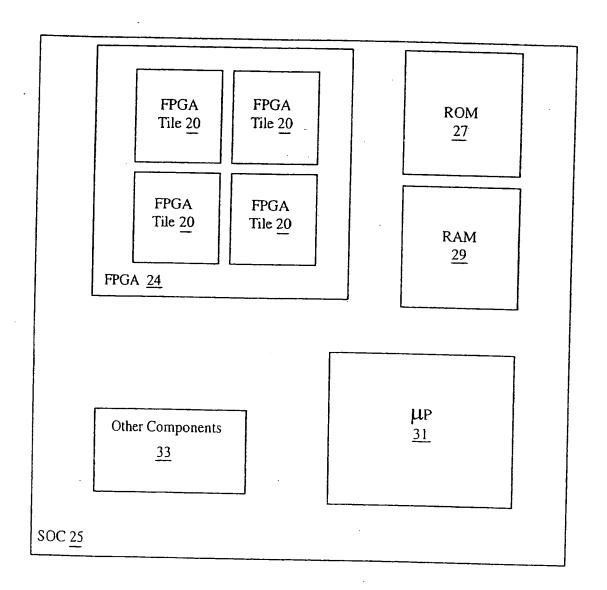


Fig. 3B

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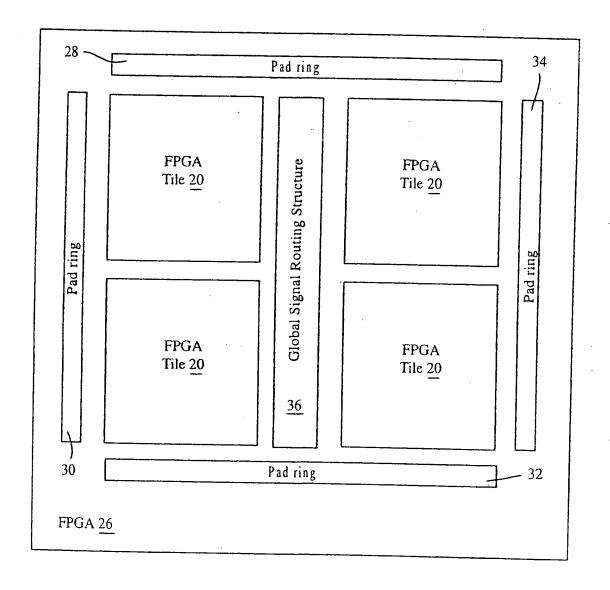


Fig.4

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Fig. 5

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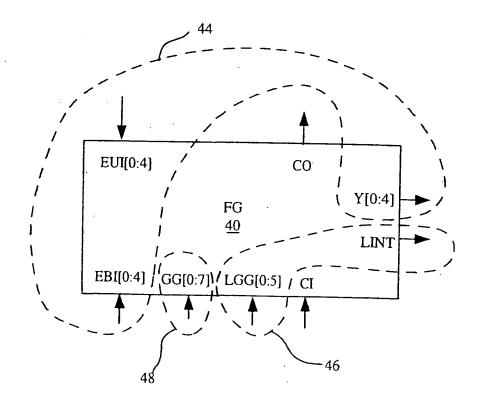


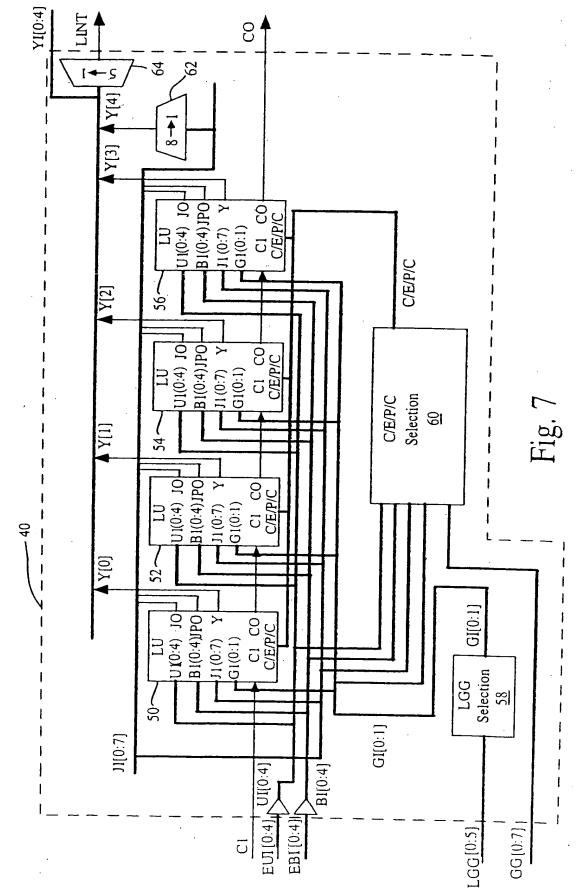
Fig. 6

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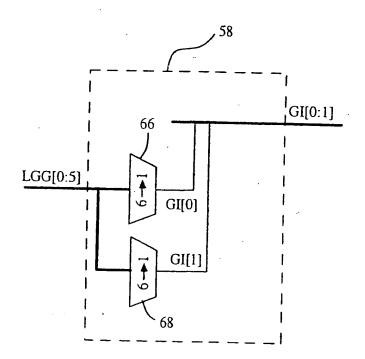
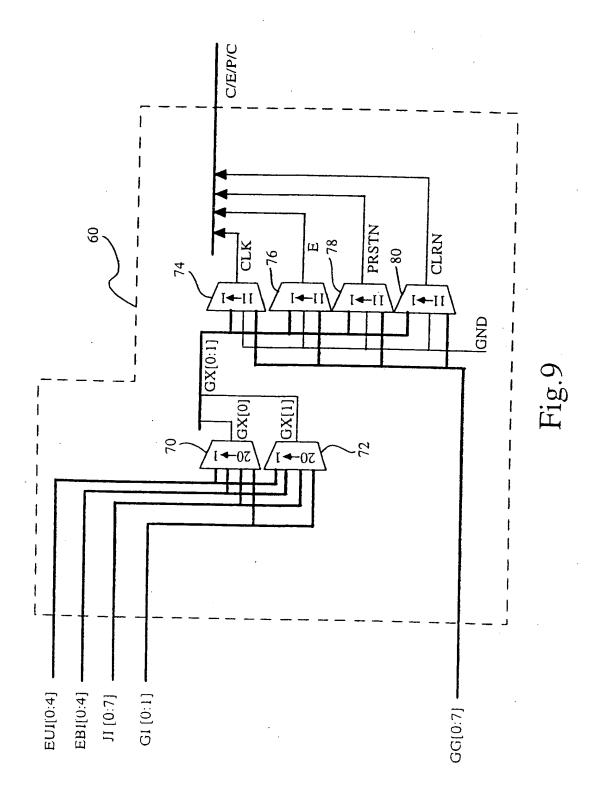


Fig.8

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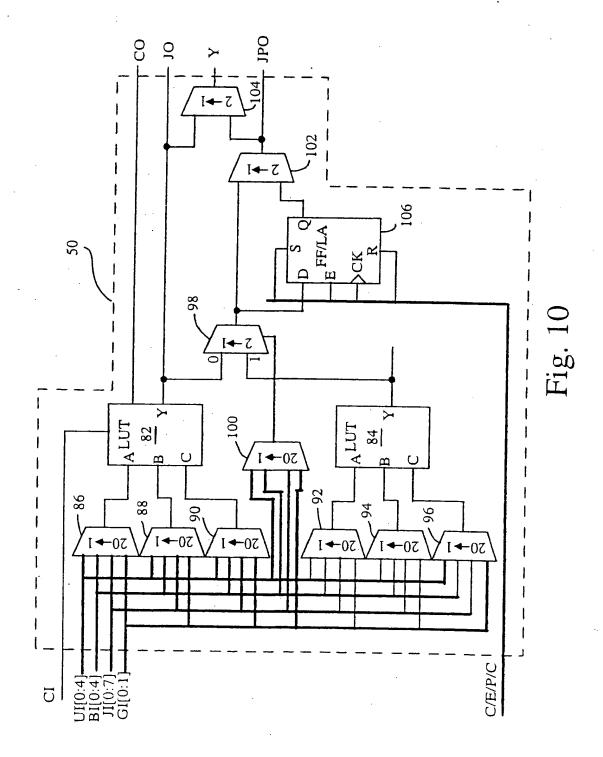
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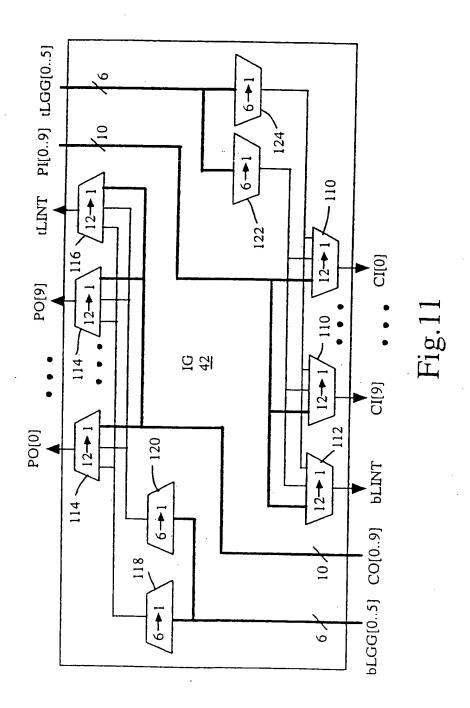
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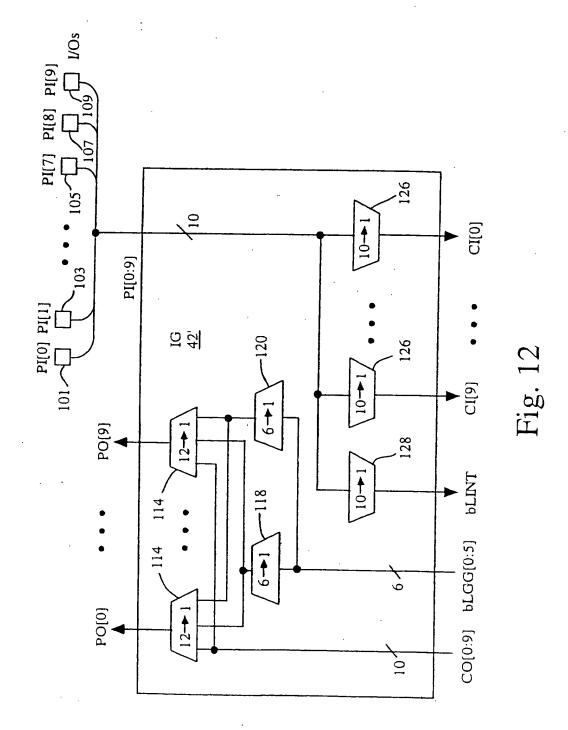
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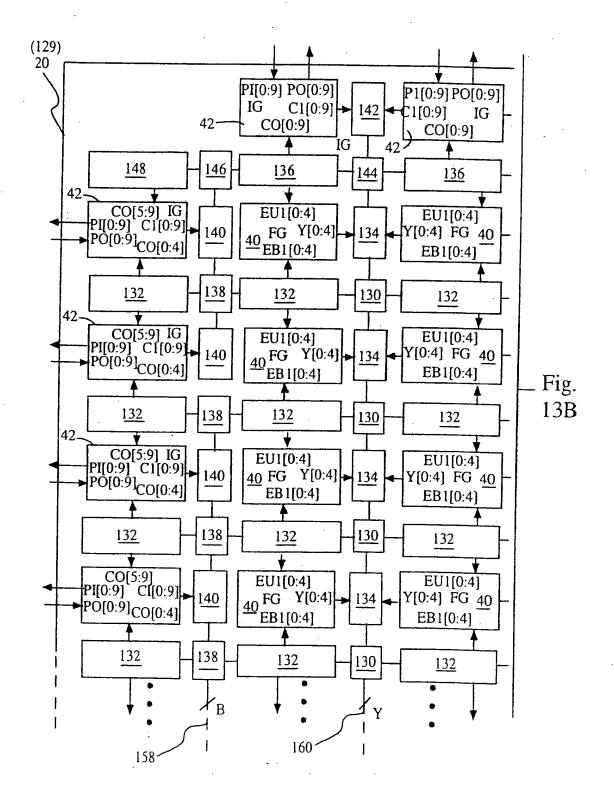
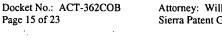


Fig. 13A

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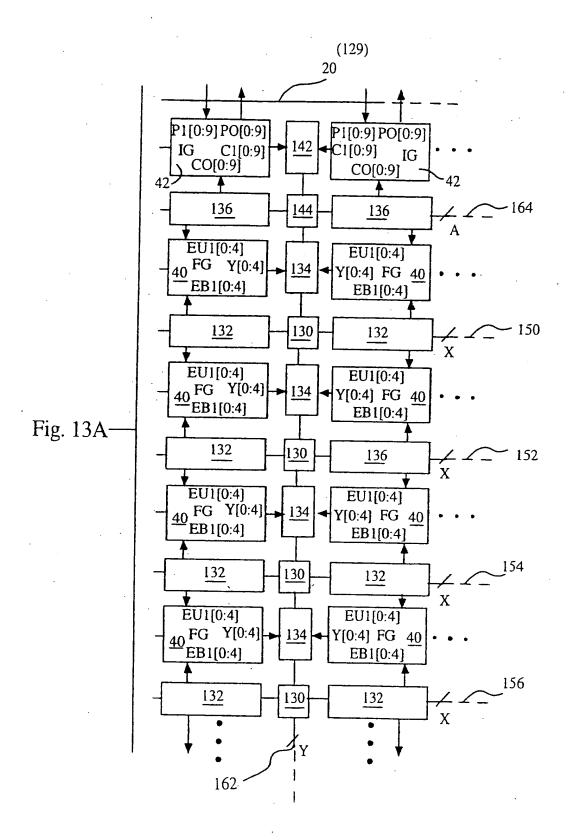


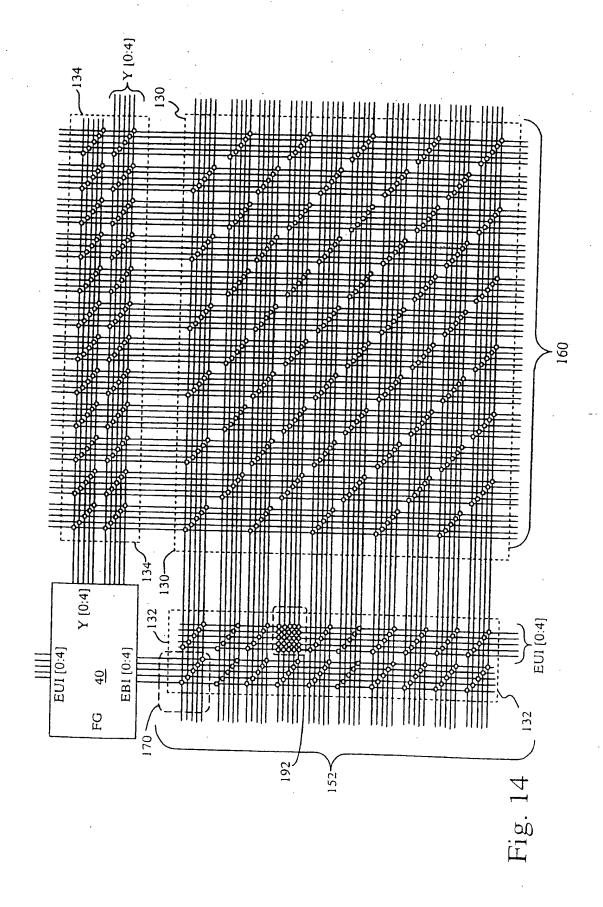
Fig. 13B

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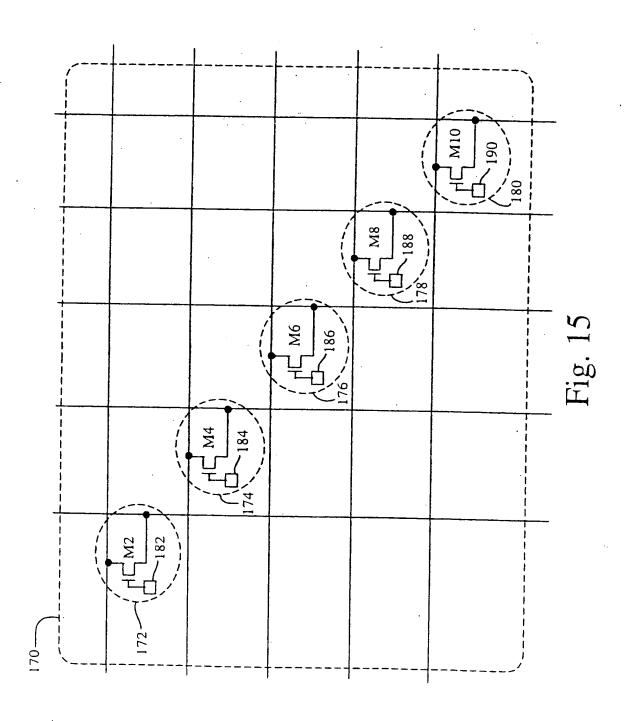
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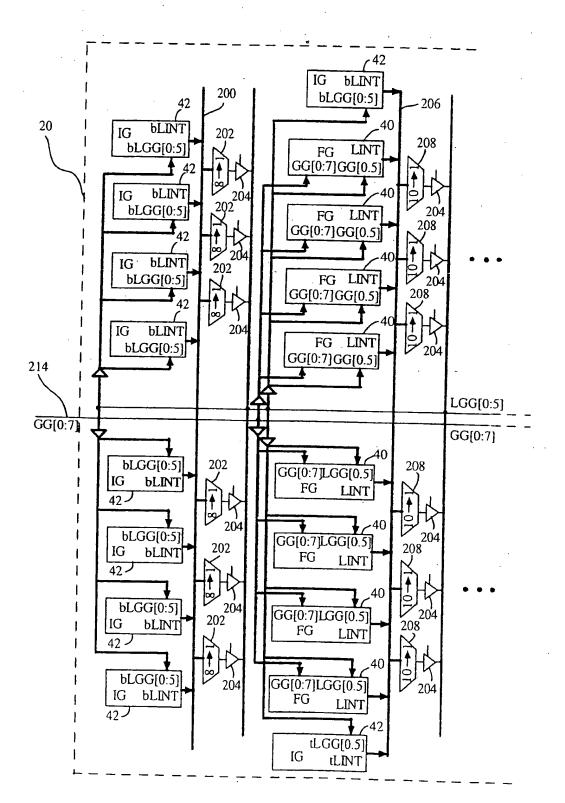


Fig. 16A

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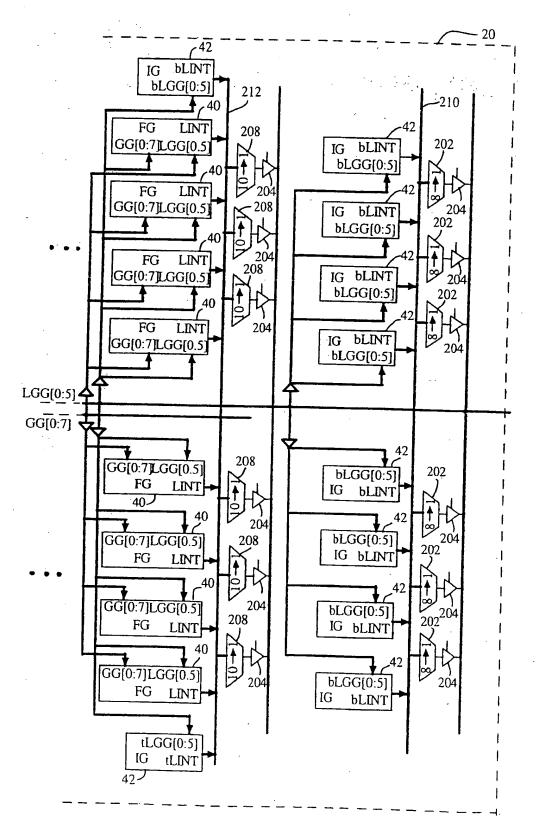


Fig. 16B

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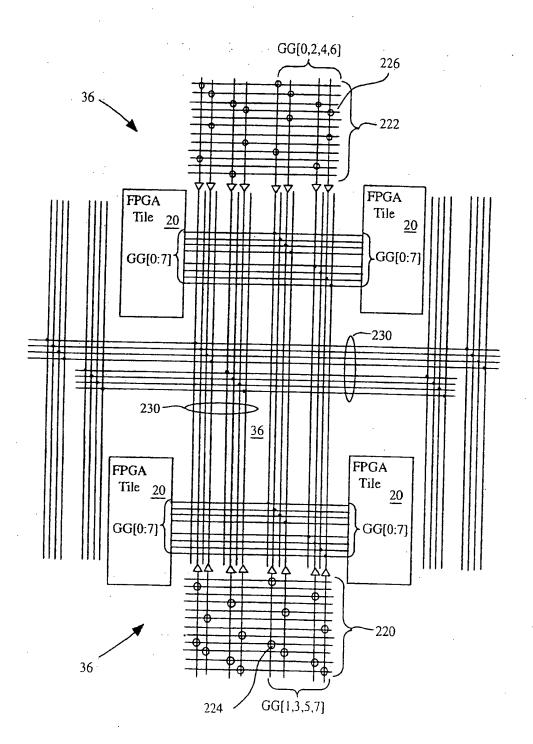
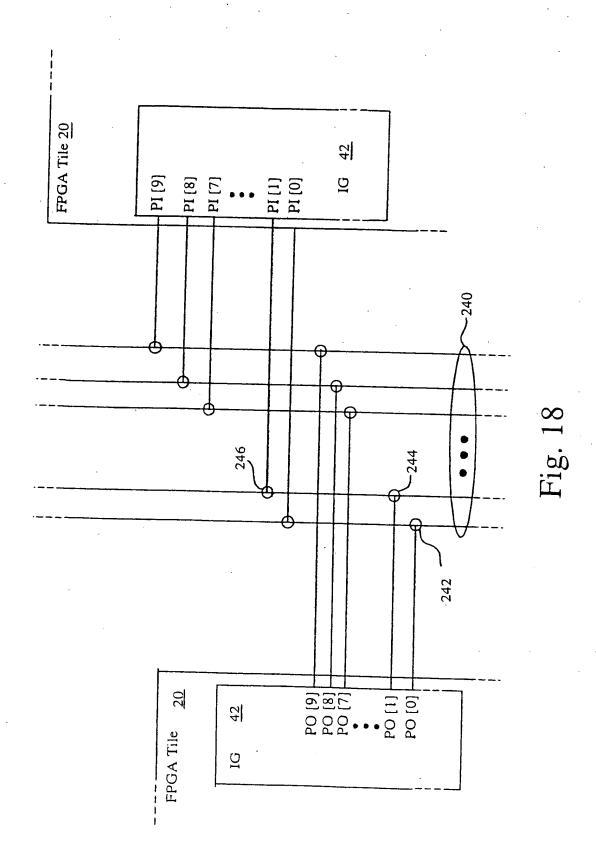


Fig. 17

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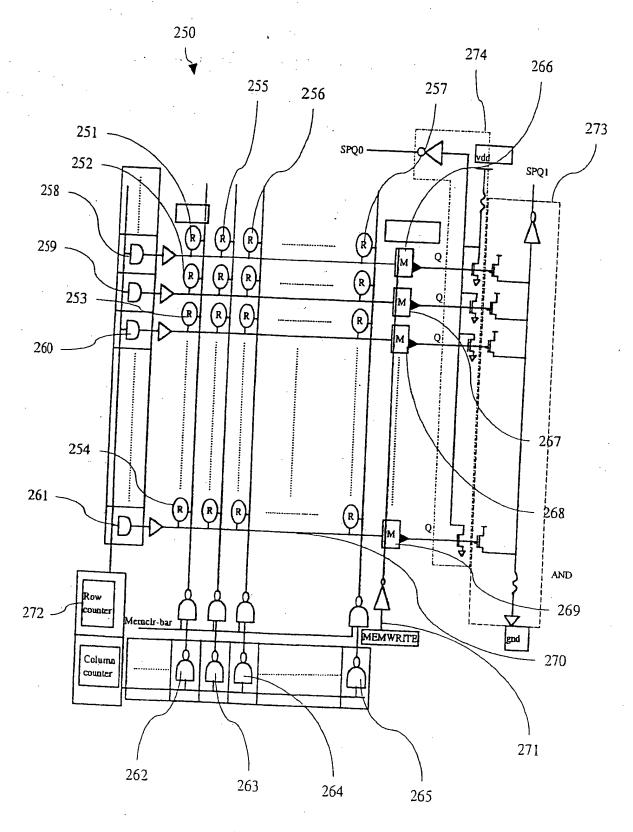


FIG. 19

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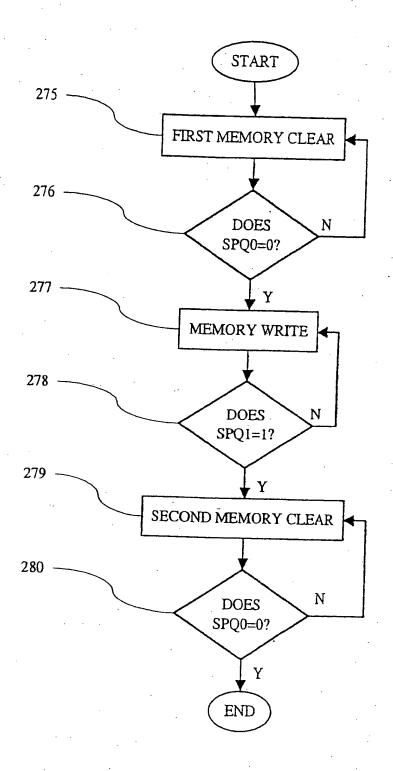


FIG. 20